

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353001 AGAWANE PRAVIN SUBHASH SUSHILA ,71413518G ,VPKBIET ,B120353001

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	32	42	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	37	59	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	42	56	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	37	56	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	36	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	39	47	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	40	61	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	40	54	P	P
404192D	WIRELESS NETWORKS	PP	100	40	16	35	51	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			29	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 951/1500 , RESULT:FIRST CLASS

B120353002 AMBLE PRATHAMESH SHIVSHANKAR PRABHAVATI ,71539567L ,VPKBIET ,B120353002

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	27	50	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	45	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	53	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	42	57	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	21	36	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	47	65	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	44	69	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	47	65	P	P
404192D	WIRELESS NETWORKS	PP	100	40	20	45	65	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1066 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& O.163]

B120353003 AMEYA MOTE VISHALAM MOTE ,71413519E ,VPKBIET ,B120353003

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	39	55	P	
404182	COMPUTER NETWORKS	PP	100	40	17	46	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	34	48	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	33	52	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	32	51	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	39	51	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	35	57	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	31	43	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	31	45	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 930/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353004 ANKIT KUMAR SINGH URMILA ,71539571J ,VPKBIET ,B120353004

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	37	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	48	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	59	83	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	41	60	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	24	49	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	37	53	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	39	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	39	55	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	37	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1068/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353005 ASHWATHI NAIR SUBHA ,71539573E ,VPKBIET ,B120353005

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	36	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	36	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	37	52	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	36	47	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	18	32	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	42	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	46	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	48	66	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	44	63	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1011/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353006 BABARE PRIYANKA DAMODAR SULOCHANA ,71413530F ,VPKBIET ,B120353006

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	34	51	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	38	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	11	43	54	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	43	59	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	43	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	40	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	33	58	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	46	62	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	47	66	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 996/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353007 BANKAR PRAJAKTA HARICHANDRA ANJALI ,71539581F ,VPKBIET ,B120353007

SEM.:1						SEM.:2												
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	35	55	P	C	404189	MOBILE COMMUNICATION	PP	100	40	11	42	53	P
404182	COMPUTER NETWORKS	PP	100	40	19	44	63	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	37	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	41	57	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	44	59	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	38	48	P	C	404192D	WIRELESS NETWORKS	PP	100	40	16	42	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	46	62	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			95	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 991/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353008 BHAGAT POOJA ANANDA ALAKA ,71638425G ,VPKBIET ,B120353008

SEM.:1						SEM.:2												
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	43	62	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	54	69	P
404182	COMPUTER NETWORKS	PP	100	40	25	48	73	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	48	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	26	53	79	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	43	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	42	57	P	C	404192D	WIRELESS NETWORKS	PP	100	40	17	50	67	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	49	68	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			95	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			44	P

GRAND TOTAL = 1096/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353009 BHANGE NITIN SIDDHESHWAR SUVARNA ,71539592M ,VPKBIET ,B120353009

SEM.:1						SEM.:2												
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	42	58	P	C	404189	MOBILE COMMUNICATION	PP	100	40	11	49	60	P
404182	COMPUTER NETWORKS	PP	100	40	22	48	70	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	45	69	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	53	70	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	37	51	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	37	55	P	C	404192D	WIRELESS NETWORKS	PP	100	40	13	43	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	46	67	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			31	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			96	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			44	P

GRAND TOTAL = 1026 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& 0.163]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353010 BHARATE MRUNALI DIPAK JYOTI ,71638426E ,VPKBIET ,B120353010

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	39	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	32	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	47	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	35	47	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	28	48	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	45	54	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	44	68	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	39	51	P	P
404192D	WIRELESS NETWORKS	PP	100	40	15	38	53	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	P
404195	PROJECT PHASE II	TW	100	40			96	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 967/1500 , RESULT:FIRST CLASS

B120353011 BHILARE SWAPNIL LAXMAN VANDANA ,71539593K ,VPKBIET ,B120353011

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	48	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	50	75	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	44	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	42	65	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	49	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	50	67	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	47	67	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	47	69	P	P
404192D	WIRELESS NETWORKS	PP	100	40	20	57	77	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = 1163/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353012 BHISE NIKITA LAXMAN SUREKHA ,71638427C ,VPKBIET ,B120353012

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	56	76	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	43	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	55	75	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	37	53	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	41	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	53	73	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	47	71	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	48	64	P	P
404192D	WIRELESS NETWORKS	PP	100	40	16	50	66	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353013 BHONG POOJA SUDHAKAR SHARADA ,71638428M ,VPKBIET ,B120353013

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	41	59	P	C	404189	MOBILE COMMUNICATION	PP	100	40	21	49	70	P	P
404182	COMPUTER NETWORKS	PP	100	40	27	38	65	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	50	76	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	58	81	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	51	74	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	38	58	P	C	404192D	WIRELESS NETWORKS	PP	100	40	16	52	68	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	25	51	76	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			93	P	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1130/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353014 BHOSALE AKASH PRATAP MATHURA ,71539601D ,VPKBIET ,B120353014

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	38	57	P	C	404189	MOBILE COMMUNICATION	PP	100	40	17	43	60	P	P
404182	COMPUTER NETWORKS	PP	100	40	24	40	64	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	43	67	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	53	72	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	42	55	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	37	52	P	C	404192D	WIRELESS NETWORKS	PP	100	40	18	49	67	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	15	42	57	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			95	P	P
404188	PROJECT PHASE I	OR	50	20			35	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1016 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& O.163]

B120353015 BHOYAR PIYUSH MANIKRAO LATA ,71313605H ,VPKBIET ,B120353015

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	45	57	P		404189	MOBILE COMMUNICATION	PP	100	40	13	29	42	P	P
404182	COMPUTER NETWORKS	PP	100	40	19	28	47	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	06	--	F	F
404183	MICROWAVE ENGINEERING	PP	100	40	14	28	42	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	34	44	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	28	44	P	C	404192D	WIRELESS NETWORKS	PP	100	40	08	28	--	F	F
404185C	PLCS AND AUTOMATION	PP	100	40	10	30	40	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			34	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	P
404188	PROJECT PHASE I	OR	50	20			35	P	C	404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353016 BHUNJE AISHWARYA NITIN SHILPA ,71638429K ,VPKBIET ,B120353016

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	47	68	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	38	56	P
404182	COMPUTER NETWORKS	PP	100	40	21	52	73	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	38	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	47	70	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	43	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	35	52	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	50	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	49	71	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			89	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			35	P

GRAND TOTAL = 1045/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353017 BICHITKAR PRIYANKA ARJUN SUNITA ,71539605G ,VPKBIET ,B120353017

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	38	58	P	C	404189	MOBILE COMMUNICATION	PP	100	40	12	39	51	P
404182	COMPUTER NETWORKS	PP	100	40	19	42	61	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	41	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	47	70	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	46	61	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	35	50	P	C	404192D	WIRELESS NETWORKS	PP	100	40	20	42	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	48	68	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			93	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			41	P

GRAND TOTAL = 1032/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353018 BIRAJDAR MANOJ NARAYAN ANURADHA ,71413565J ,VPKBIET ,B120353018

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	39	60	P	C	404189	MOBILE COMMUNICATION	PP	100	40	19	49	68	P
404182	COMPUTER NETWORKS	PP	100	40	18	36	54	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	38	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	45	63	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	41	51	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	33	50	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	46	65	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	44	67	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			95	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 1002/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353019 BIRAJDAR SANGITA CHANDRAKANT SUREKHA ,71638430C ,VPKBIET ,B120353019

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	54	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	39	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	58	81	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	40	58	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	43	66	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	52	71	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	52	75	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	52	65	P	P
404192D	WIRELESS NETWORKS	PP	100	40	17	48	65	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404195	PROJECT PHASE II	TW	100	40			93	P	P
404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353020 BOBADE AKSHAY AVINASH SHUBHANGI ,71539608M ,VPKBIET ,B120353020

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	37	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	49	71	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	52	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	33	47	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	33	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	47	60	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	41	66	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	36	46	P	P
404192D	WIRELESS NETWORKS	PP	100	40	12	35	47	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404195	PROJECT PHASE II	TW	100	40			93	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 976 + 10/1500 , RESULT:FIRST CLASS [& O.163]

B120353021 BORLEPWAR KAUSTUBH SHIVKUMAR RAJANI ,71539612K ,VPKBIET ,B120353021

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	41	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	48	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	38	52	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	33	46	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	42	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	51	67	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	35	60	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	46	60	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	45	59	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			37	P	P

GRAND TOTAL = 992/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353022 CHAMWAD AMAR ANGAD REKHA ,71539613H ,VPKBIET ,B120353022

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	39	59	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	50	73	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	40	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	31	47	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	46	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	40	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	43	69	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	42	57	P	
404192D	WIRELESS NETWORKS	PP	100	40	15	46	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			37	P	

GRAND TOTAL = 1018/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353023 CHAUDHARI SHRADDHA SANJAY SUNITA ,71539617L ,VPKBIET ,B120353023

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	30	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	44	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	32	48	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	29	44	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	16	28	44	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	40	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	42	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	43	58	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	43	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 956/1500 , RESULT:FIRST CLASS

B120353024 CHAVAN MAHENDRA SURESH NANDABAI ,71539620L ,VPKBIET ,B120353024

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	28	40	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	36	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	38	53	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	33	45	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	31	45	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			27	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	36	47	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	33	55	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	41	51	P	
404192D	WIRELESS NETWORKS	PP	100	40	13	37	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 872/1500 , RESULT:HIGHER SECOND CLASS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353025 CHAVAN SANDHYA SUBHASH KAVITA ,71539621J ,VPKBIET ,B120353025

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	32	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	46	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	47	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	34	48	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	21	45	66	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	46	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	41	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	38	51	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	28	45	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			35	P	

GRAND TOTAL = 970/1500 , RESULT:FIRST CLASS

B120353026 CHAVLE ANJALI BALIRAM KAMALABAI ,71539623E ,VPKBIET ,B120353026

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	31	46	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	28	40	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	28	40	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	20	--	F	
404185C	PLCS AND AUTOMATION	PP	100	40	11	25	--	F	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	31	45	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	37	60	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	09	31\$	40	P	
404192D	WIRELESS NETWORKS	PP	100	40	09	17	--	F	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120353027 CHONDE SATISH UDHAVRAO CHHAYA ,71212392L ,VPKBIET ,B120353027

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	33	43	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	31	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	41	54	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	29	41	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	15	29	44	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			32	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			33	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	35	43	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	31	55	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	38	50	P	
404192D	WIRELESS NETWORKS	PP	100	40	08	32\$	40	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 869/1500 , RESULT:HIGHER SECOND CLASS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353028 DUKARE OM DILIPRAO SANGITA ,71539659F ,VPKBIET ,B120353028

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	30	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	46	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	44	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	32	47	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	45	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	52	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	41	68	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	42	54	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	44	62	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 995/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353029 FULARI AISHWARYA NAMDEO SUVARNA ,71638435D ,VPKBIET ,B120353029

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	36	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	51	75	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	60	76	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	37	54	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	50	76	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	51	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	51	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	50	67	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	51	70	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353030 FULARI UTKARSHA NAMDEO SUVARNA ,71638436B ,VPKBIET ,B120353030

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	41	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	53	77	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	56	74	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	42	58	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	21	56	77	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	53	71	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	56	82	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	52	71	P	
404192D	WIRELESS NETWORKS	PP	100	40	14	53	67	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1140/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353031 GAIKWAD PRARTHANA RAJENDRA ANITA ,71539665L ,VPKBIET ,B120353031

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	40	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	39	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	44	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	33	45	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	13	36	49	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	33	48	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	43	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	37	49	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	42	58	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 962/1500 , RESULT:FIRST CLASS

B120353032 GAIKWAD SHITAL SURESH KUSUM ,71638437L ,VPKBIET ,B120353032

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	53	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	52	77	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	54	75	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	49	74	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	24	59	83	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	38	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	41	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	48	63	P	
404192D	WIRELESS NETWORKS	PP	100	40	23	49	72	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1117/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353033 GAWADE ARCHANA SAHEBRAO REKHA ,71638438J ,VPKBIET ,B120353033

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	56	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	60	86	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	57	73	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	37	55	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	59	81	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	43	62	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	54	80	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	52	68	P	
404192D	WIRELESS NETWORKS	PP	100	40	21	48	69	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1115/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353034 GAWADE MAYURI RAMESH UJWALA ,71539670G ,VPKBIET ,B120353034

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	51	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	45	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	48	73	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	35	47	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	50	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	44	56	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	49	73	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	45	60	P	P
404192D	WIRELESS NETWORKS	PP	100	40	19	41	60	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1064/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353035 GHADAGE AMOL ARUN LATA ,71638439G ,VPKBIET ,B120353035

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	52	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	54	75	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	48	67	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	48	74	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	24	58	82	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	41	50	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	50	73	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	49	62	P	P
404192D	WIRELESS NETWORKS	PP	100	40	20	41	61	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			39	P	P

GRAND TOTAL = 1082/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353036 GHOLAP AISHWARYA RAVINDRA ARCHANA ,71539679L ,VPKBIET ,B120353036

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	40	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	40	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	51	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	40	55	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	49	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	31	42	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	41	66	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	39	52	P	P
404192D	WIRELESS NETWORKS	PP	100	40	19	34	53	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 999/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353037 GHOTALE GITESH MANIKRAO JAYSHREE ,71539680D ,VPKBIET ,B120353037

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	43	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	31	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	54	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	42	52	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	50	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	07	33\$	40	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	40	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	40	52	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	37	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			27	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS [\$ 0.1]

B120353038 GHOTALE RITESH MANIKRAO JAYSHREE ,71539681B ,VPKBIET ,B120353038

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	45	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	30	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	49	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	09	44	53	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	53	70	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	35	45	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	46	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	39	51	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	39	57	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 987/1500 , RESULT:FIRST CLASS

B120353039 GORE BALASAHEB RAMESHRAO BHAGIRATHI ,71539685E ,VPKBIET ,B120353039

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	46	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	45	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	41	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	41	59	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	55	75	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	16	--	F	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	38	58	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	34	49	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	38	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353040 GURME ROHINI BABURAO ANITA ,71539688K ,VPKBIET ,B120353040

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	39	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	11	29	40	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	03	28	--	F	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	32	40	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	29	46	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			28	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	03	28	--	F	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	28	47	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	35	43	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	33	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			32	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

B120353041 HALDAR SUFIYAN SALIM HASINA ,71539689H ,VPKBIET ,B120353041

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	55	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	53	76	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	56	78	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	51	76	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	25	60	85	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	44	62	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	50	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	45	60	P	
404192D	WIRELESS NETWORKS	PP	100	40	20	39	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1159/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353042 HARIHAR VARSHA KAILAS KAMAL ,71638441J ,VPKBIET ,B120353042

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	45	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	56	82	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	56	72	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	55	65	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	59	81	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			29	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	43	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	56	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	51	65	P	
404192D	WIRELESS NETWORKS	PP	100	40	22	49	71	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1084/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353043 INGALE POOJA DATTATRAYA REKHA ,71638442G ,VPKBIET ,B120353043

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	52	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	51	74	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	53	75	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	49	75	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	59	82	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	40	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	47	72	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	44	58	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	47	65	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353044 INGAWALE SUPRIYA PRABHAKAR SHAKUNTLA ,71638443E ,VPKBIET ,B120353044

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	42	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	43	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	57	79	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	43	56	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	56	79	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	29	46	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	48	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	43	55	P	
404192D	WIRELESS NETWORKS	PP	100	40	23	45	68	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			27	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1046/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353045 JADHAV GITANJALI GOVARDHAN ARCHANA ,71638444C ,VPKBIET ,B120353045

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	53	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	51	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	59	82	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	54	72	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	59	85	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	42	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	51	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	43	55	P	
404192D	WIRELESS NETWORKS	PP	100	40	23	44	67	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1131/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353046 JADHAV SOURABH SANJEEV SUNITA ,71539706M ,VPKBIET ,B120353046

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	46	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	47	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	51	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	60	75	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	59	79	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	41	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	50	77	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	43	56	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	43	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1091/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353047 JADHAV SURAJ VIJAY SANGITA ,71638445M ,VPKBIET ,B120353047

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	39	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	46	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	56	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	47	57	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	21	57	78	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	35	45	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	53	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	09	36	45	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	38	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1000/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353048 JAGDALE NIKITA NANASAHEB ALKA ,71638446K ,VPKBIET ,B120353048

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	45	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	44	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	54	78	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	47	63	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	60	86	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	40	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	50	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	37	50	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	37	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1069/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353049 JAGTAP NILESH VIJAY VAISHALI ,71638447H ,VPKBIET ,B120353049

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	51	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	45	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	54	73	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	52	71	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	54	80	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	34	46	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	45	71	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	40	54	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	37	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1033/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353050 JAGTAP VRUKSHANDA GAUTAM URMILA ,71638448F ,VPKBIET ,B120353050

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	50	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	53	77	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	53	66	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	52	67	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	27	60	87	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	35	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	50	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	46	60	P	
404192D	WIRELESS NETWORKS	PP	100	40	22	46	68	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			29	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353051 JOGDAND SARIKA SHAHADEO ANITA ,71539719C ,VPKBIET ,B120353051

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	41	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	47	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	46	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	54	70	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	56	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	30	47	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	40	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	41	56	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	40	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1058/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353052 JOSHI SHIVANI SHRIKRISHNA MANJIRI ,71539720G ,VPKBIET ,B120353052

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	52	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	49	71	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	53	74	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	48	74	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	52	74	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	41	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	48	68	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	41	53	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	36	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1098/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353053 KAKADE ASHOK PRUTHVIRAJ LATABAI ,71539725H ,VPKBIET ,B120353053

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	42	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	50	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	58	77	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	46	63	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	53	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			33	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	41	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	51	77	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	31	45	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	38	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			37	P	

GRAND TOTAL = 1034/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353054 KALYANI RENUKA CHANDRAKANT ASHA ,71539729L ,VPKBIET ,B120353054

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	42	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	45	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	52	66	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	54	66	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	40	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	38	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	48	74	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	41	54	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	38	56	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353055 KANHERKAR PRADNYA DATTATRAY JAYSHREE ,71539736C ,VPKBIET ,B120353055

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	41	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	50	71	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	46	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	56	76	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	18	44	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	39	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	39	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	46	59	P	
404192D	WIRELESS NETWORKS	PP	100	40	21	41	62	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1076/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353056 KANWATE DINESH VINAYAKRAO REKHA ,71413708B ,VPKBIET ,B120353056

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	33	45	P	
404182	COMPUTER NETWORKS	PP	100	40	15	33	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	08	33	41	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	11	34	45	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	09	39	48	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			26	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			25	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			33	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	35	48	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	35	55	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	09	36	45	P	
404192D	WIRELESS NETWORKS	PP	100	40	11	37	48	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			25	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			10	F	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			12	F	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			30	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120353057 KARANJE POOJA VIJAY VARASHA ,71505067C ,VPKBIET ,B120353057

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	34	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	47	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	49	61	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	12	42	54	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	47	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			32	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	30	40	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	38	57	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	42	54	P	
404192D	WIRELESS NETWORKS	PP	100	40	20	39	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			31	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			27	P	
404195	PROJECT PHASE II	TW	100	40			84	P	
404195	PROJECT PHASE II	PR	50	20			36	P	

GRAND TOTAL = 912/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353058 KASALE SHUBHAM SURESH BHUMIKA ,71539741K ,VPKBIET ,B120353058

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	36	49	P	C	404189	MOBILE COMMUNICATION	PP	100	40	12	37	49	P
404182	COMPUTER NETWORKS	PP	100	40	20	40	60	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	41	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	08	43	51	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	47	55	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	39	57	P	C	404192D	WIRELESS NETWORKS	PP	100	40	17	40	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	49	67	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C	404195	PROJECT PHASE II	TW	100	40			90	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			38	P

GRAND TOTAL = 947/1500 , RESULT:FIRST CLASS

B120353059 KATKAR POOJA SHASHIKANT SUVARNA ,71638450H ,VPKBIET ,B120353059

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	35	52	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	33	51	P
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	44	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	49	63	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	47	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	49	64	P	C	404192D	WIRELESS NETWORKS	PP	100	40	21	44	65	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	50	72	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			89	P
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = 1052/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353060 KHEMNAR KIRAN BHAUSAHEB ALKA ,71539754M ,VPKBIET ,B120353060

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	38	56	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	28	43	P
404182	COMPUTER NETWORKS	PP	100	40	20	38	58	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	33	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	40	56	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	41	53	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	35	46	P	C	404192D	WIRELESS NETWORKS	PP	100	40	16	43	59	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	49	67	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			95	P
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			43	P

GRAND TOTAL = 968/1500 , RESULT:FIRST CLASS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353061 KHOMANE VISHRANTI BHAGWAN ARUNA ,71638452D ,VPKBIET ,B120353061

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	52	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	37	59	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	38	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	49	60	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	16	47	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	48	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	54	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	53	66	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	40	56	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1045/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353062 KHUTALE SACHIN PRAKASH SUNANDA ,71539760F ,VPKBIET ,B120353062

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	58	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	28	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	43	60	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	48	74	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	21	33	54	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	48	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	39	60	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	42	55	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	30	47	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1005/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353063 KIRVE SHAMALI RAMESH VANITA ,71539761D ,VPKBIET ,B120353063

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	35	46	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	28	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	33	50	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	41	51	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	16	35	51	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	47	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	40	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	47	59	P	
404192D	WIRELESS NETWORKS	PP	100	40	21	34	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 976/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353064 KOLAPE ASHOK MAHADEO MANGAL ,71539764J ,VPKBIET ,B120353064

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	58	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	32	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	41	61	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	50	69	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	43	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	48	60	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	50	75	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	52	67	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	30	44	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1052/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353065 KUMBHAR RUTUJA SUNIL SADHANA ,71638453B ,VPKBIET ,B120353065

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	56	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	38	61	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	50	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	67	85	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	44	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	50	66	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	51	77	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	57	70	P	P
404192D	WIRELESS NETWORKS	PP	100	40	21	52	73	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1143/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353066 KUMBHARKAR LAXMIKANT KISHOR MAHANANDA ,71413735K ,VPKBIET ,B120353066

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	33	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	39	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	30	40	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	45	56	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	16	40	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	35	48	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	37	60	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	46	58	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	34	48	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 939/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353067 LIGADE SNEHAL CHANDRAKANT SAVITA ,71638454L ,VPKBIET ,B120353067

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	53	71	P	C	404189	MOBILE COMMUNICATION	PP	100	40	12	55	67	P	P
404182	COMPUTER NETWORKS	PP	100	40	23	35	58	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	48	73	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	43	58	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	46	58	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	53	69	P	C	404192D	WIRELESS NETWORKS	PP	100	40	16	38	54	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	40	62	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			89	P	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 1041/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353068 LUNGARE BALIRAM GANGADHAR SUREKHA ,71413742B ,VPKBIET ,B120353068

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	39	53	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	34	48	P	P
404182	COMPUTER NETWORKS	PP	100	40	21	31	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	41	66	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	09	36	45	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	48	58	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	39	51	P	C	404192D	WIRELESS NETWORKS	PP	100	40	16	28	44	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	29	48	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			90	P	P
404188	PROJECT PHASE I	OR	50	20			33	P	C	404195	PROJECT PHASE II	PR	50	20			32	P	P

GRAND TOTAL = 906/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

B120353069 MALGUNDE MAYURI DATTU SANGITA ,71539784C ,VPKBIET ,B120353069

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	49	69	P	C	404189	MOBILE COMMUNICATION	PP	100	40	10	33	43	P	P
404182	COMPUTER NETWORKS	PP	100	40	15	35	50	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	46	70	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	28	42	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	46	61	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	33	41	P	C	404192D	WIRELESS NETWORKS	PP	100	40	15	29	44	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	29	45	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			29	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C	404195	PROJECT PHASE II	TW	100	40			93	P	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 924/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353071 MANE PALLAVI TUKARAM SHANTABAI ,71539788F ,VPKBIET ,B120353071

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	48	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	35	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	38	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	48	62	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	35	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	45	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	53	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	55	68	P	
404192D	WIRELESS NETWORKS	PP	100	40	15	38	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1025/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353072 MANE PRERANA SACHIN KUSUM ,71539789D ,VPKBIET ,B120353072

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	45	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	33	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	38	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	49	61	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	16	32	48	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	51	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	43	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	49	62	P	
404192D	WIRELESS NETWORKS	PP	100	40	12	39	51	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 986/1500 , RESULT:FIRST CLASS

B120353073 MANE SNEHA DNYANESHVAR NILAVATI ,71539790H ,VPKBIET ,B120353073

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	46	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	31	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	47	68	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	41	52	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	37	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	44	53	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	41	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	44	54	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	39	58	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353074 MANE SWAPNALI RAJENDRA USHA ,71638455J ,VPKBIET ,B120353074

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	55	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	39	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	58	83	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	59	77	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	25	34	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	44	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	50	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	44	61	P	
404192D	WIRELESS NETWORKS	PP	100	40	25	38	63	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1148/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353075 MEMANE RUSHIKESH ROHIDAS RANJANA ,71539796G ,VPKBIET ,B120353075

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	49	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	30	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	43	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	54	70	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	43	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	41	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	40	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	47	57	P	
404192D	WIRELESS NETWORKS	PP	100	40	12	32	44	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353076 MULLA MINAJ RAJMAHAMAD SHAHANAJ ,71638456G ,VPKBIET ,B120353076

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	50	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	39	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	61	82	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	48	64	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	36	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	48	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	46	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	50	63	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	46	64	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			37	P	

GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353077 NAGALE AMRUTA RAJENDRA PUSHPA ,71638457E ,VPKBIET ,B120353077

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	55	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	35	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	56	79	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	55	72	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	25	49	74	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	60	81	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	50	77	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	47	61	P	P
404192D	WIRELESS NETWORKS	PP	100	40	17	51	68	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			96	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1170/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353078 NALAWADE SONALI BALASAHEB SANGITA ,71539810F ,VPKBIET ,B120353078

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	40	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	45	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	57	79	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	45	63	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	35	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	37	48	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	49	76	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	49	65	P	P
404192D	WIRELESS NETWORKS	PP	100	40	19	30	49	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1060/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353079 NALE SHUBHANGI RAJENDRA VAISHALI ,71638458C ,VPKBIET ,B120353079

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	55	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	37	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	60	79	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	55	74	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	42	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	56	71	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	53	77	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	59	74	P	P
404192D	WIRELESS NETWORKS	PP	100	40	19	36	55	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1127/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353080 NANDGUDE AISHVARYA CHHAGAN RANJANA ,71539814J ,VPKBIET ,B120353080

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	60	82	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	42	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	60	81	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	63	80	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	39	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	48	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	53	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	56	73	P	
404192D	WIRELESS NETWORKS	PP	100	40	13	45	58	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1132/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353081 NARGIDE JYOTI SUNIL USHA ,71539815G ,VPKBIET ,B120353081

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	50	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	40	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	60	86	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	60	77	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	24	39	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			32	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	42	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	50	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	52	65	P	
404192D	WIRELESS NETWORKS	PP	100	40	20	52	72	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			30	P	

GRAND TOTAL = 1090 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& O.163]

B120353082 NAVTAKE SATYAM RAMAKANT DHANSHREE ,71638459M ,VPKBIET ,B120353082

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	31	44	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	29	49	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	11	42	53	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	44	64	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	32	54	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	11	--	F	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	28	42	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	34	46	P	
404192D	WIRELESS NETWORKS	PP	100	40	12	31	43	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353083 NAZIRKAR DIPTI CHANDRAKANT KALINDI ,71539816E ,VPKBIET ,B120353083

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	58	81	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	43	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	57	77	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	60	75	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	24	34	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	48	62	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	50	77	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	53	68	P	P
404192D	WIRELESS NETWORKS	PP	100	40	21	42	63	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1115/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353084 PALANGE SAMPADA RAVINDRA ASAVARI ,71539890D ,VPKBIET ,B120353084

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	37	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	33	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	33	49	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	31	50	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	15	52	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	32	42	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	44	69	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	38	50	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	37	51	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 995/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353085 PANCHVISHE SHRIDEVI SIDDESHWAR SUNANDA ,71539830L ,VPKBIET ,B120353085

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	45	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	34	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	50	72	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	56	72	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	25	39	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	40	51	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	47	73	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	51	64	P	P
404192D	WIRELESS NETWORKS	PP	100	40	18	46	64	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			96	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1076/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353086 PANDIT TUSHAR SHARAD SAUDAMINI ,71505087H ,VPKBIET ,B120353086

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	39	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	29	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	49	61	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	46	65	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	28	47	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	06	17	--	F
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	33	55	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	40	52	P
404192D	WIRELESS NETWORKS	PP	100	40	10	34	44	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P
404195	PROJECT PHASE II	TW	100	40			90	P
404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = --/1500 , RESULT:FAILS

B120353087 PARASKAR MAHENDRA MAHADEV MEENA ,71539834C ,VPKBIET ,B120353087

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	45	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	35	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	45	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	43	56	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	34	51	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			33	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	33	45	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	40	64	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	50	60	P
404192D	WIRELESS NETWORKS	PP	100	40	17	34	51	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P
404195	PROJECT PHASE II	TW	100	40			80	P
404195	PROJECT PHASE II	PR	50	20			37	P

GRAND TOTAL = 936/1500 , RESULT:FIRST CLASS

B120353088 PATIL AADITI NAGESHRAO MADHAVI ,71638460E ,VPKBIET ,B120353088

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	60	81	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	45	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	58	80	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	64	82	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	27	37	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	37	54	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	50	76	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	50	68	P
404192D	WIRELESS NETWORKS	PP	100	40	21	54	75	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404195	PROJECT PHASE II	TW	100	40			97	P
404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 1160/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353089 PATIL BHAGYASHRI HILAL VANDANA ,71539841F ,VPKBIET ,B120353089

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	39	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	31	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	45	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	36	44	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	16	28	44	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	32	40	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	29	53	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	44	56	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	39	53	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 950 + 10/1500 , RESULT:FIRST CLASS [& O.163]

B120353090 PATIL NEHA PRAKASH SANGITA ,71638461C ,VPKBIET ,B120353090

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	53	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	30	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	51	70	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	52	71	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	26	38	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	39	49	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	43	66	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	55	70	P	P
404192D	WIRELESS NETWORKS	PP	100	40	17	37	54	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			94	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1072/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353091 PATIL SNEHAL PRAKASH MAHADEVI ,71638462M ,VPKBIET ,B120353091

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	46	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	44	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	51	78	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	24	54	78	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	62	85	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	43	55	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	47	71	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	56	73	P	P
404192D	WIRELESS NETWORKS	PP	100	40	19	45	64	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1142/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353092 PAWAR CHAITANYA NANDAKUMAR RANJANA ,71539856D ,VPKBIET ,B120353092

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	44	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	30	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	42	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	43	58	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	12	37	49	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	31	41	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	49	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	44	57	P	
404192D	WIRELESS NETWORKS	PP	100	40	15	37	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 989/1500 , RESULT:FIRST CLASS

B120353093 PAWAR KAJAL ANIL LATA ,71638464H ,VPKBIET ,B120353093

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	42	57	P	
404182	COMPUTER NETWORKS	PP	100	40	23	40	63	P	
404183	MICROWAVE ENGINEERING	PP	100	40	17	31	48	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	44	63	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	41	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	41	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	44	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	11	36	47	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	40	57	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 981/1500 , RESULT:FIRST CLASS

B120353094 PAWAR KOMAL HANMANT CHANDA ,71413817H ,VPKBIET ,B120353094

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	42	53	P	
404182	COMPUTER NETWORKS	PP	100	40	19	34	53	P	
404183	MICROWAVE ENGINEERING	PP	100	40	18	28	46	P	
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	43	59	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	36	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	31	40	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	34	58	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	38	48	P	
404192D	WIRELESS NETWORKS	PP	100	40	14	32	46	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			29	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			37	P	

GRAND TOTAL = 893 + 07/1500 , RESULT:FIRST CLASS [0.2]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353095 PAWAR PRATIKSHA BALASAHEB SUNITA ,71638465F ,VPKBIET ,B120353095

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	36	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	36	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	46	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	64	86	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	24	61	85	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	33	52	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	50	74	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	48	63	P	P
404192D	WIRELESS NETWORKS	PP	100	40	17	43	60	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404195	PROJECT PHASE II	TW	100	40			97	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = 1137/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353096 POTE MOHINI SAMBHAJI ARUNA ,71539868H ,VPKBIET ,B120353096

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	34	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	32	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	35	56	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	49	64	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	53	76	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	36	55	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	53	78	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	48	63	P	P
404192D	WIRELESS NETWORKS	PP	100	40	13	42	55	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353097 RAKHUNDE ANANDA SADASHIV RANJANA ,71413835F ,VPKBIET ,B120353097

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	30	48	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	35	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	37	59	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	46	67	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	43	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	36	50	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	47	70	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	43	55	P	P
404192D	WIRELESS NETWORKS	PP	100	40	14	39	53	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 984/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353099 SABALE PRIYANKA VILAS SHUBHANGI ,71539881E ,VPKBIET ,B120353099

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	40	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	33	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	53	80	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	60	78	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	52	74	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	37	53	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	53	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	52	67	P	
404192D	WIRELESS NETWORKS	PP	100	40	19	45	64	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1106/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353100 SADAWARTE PRITI SAMBHAJI MANISHA ,71539883M ,VPKBIET ,B120353100

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	36	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	32	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	52	75	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	48	65	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	46	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	41	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	50	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	52	64	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	36	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1043/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353101 SAKATE DHANASHRI RAJIV VAISHALI ,71505096G ,VPKBIET ,B120353101

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	43	54	P	
404182	COMPUTER NETWORKS	PP	100	40	22	34	56	P	
404183	MICROWAVE ENGINEERING	PP	100	40	22	31	53	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	35	50	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	11	38	49	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	06	28	34#	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	40	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	07	33	40	P	
404192D	WIRELESS NETWORKS	PP	100	40	12	34	46	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			29	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 875/1500 , RESULT:HIGHER SECOND CLASS [# 0.4]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353102 SHAIKH SHERMOHAMMAD RAUF SHAHIN ,71539911L ,VPKBIET ,B120353102

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	44	63	P
404182	COMPUTER NETWORKS	PP	100	40	17	28	45	P C
404183	MICROWAVE ENGINEERING	PP	100	40	09	36	45	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	45	57	P C
404185C	PLCS AND AUTOMATION	PP	100	40	20	48	68	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			28	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			23	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P C
404188	PROJECT PHASE I	OR	50	20			30	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	30	42	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	40	59	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	38	46	P
404192D	WIRELESS NETWORKS	PP	100	40	17	37	54	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P
404195	PROJECT PHASE II	TW	100	40			90	P
404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 920/1500 , RESULT:FIRST CLASS

B120353103 SHELAR PRADNYA DATTATRAYA MANISHA ,71539914E ,VPKBIET ,B120353103

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	43	61	P C
404182	COMPUTER NETWORKS	PP	100	40	24	37	61	P C
404183	MICROWAVE ENGINEERING	PP	100	40	25	50	75	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	59	75	P C
404185C	PLCS AND AUTOMATION	PP	100	40	26	56	82	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P C
404188	PROJECT PHASE I	OR	50	20			41	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	46	59	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	62	83	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	54	69	P
404192D	WIRELESS NETWORKS	PP	100	40	26	46	72	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404195	PROJECT PHASE II	TW	100	40			91	P
404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 1134/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353104 SHEMBALE NITIN BALAJI SUMAN ,71539915C ,VPKBIET ,B120353104

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	37	54	P C
404182	COMPUTER NETWORKS	PP	100	40	24	32	56	P C
404183	MICROWAVE ENGINEERING	PP	100	40	20	47	67	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	44	55	P C
404185C	PLCS AND AUTOMATION	PP	100	40	24	62	86	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P C
404188	PROJECT PHASE I	OR	50	20			40	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	29	43	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	47	71	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	54	68	P
404192D	WIRELESS NETWORKS	PP	100	40	18	38	56	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404195	PROJECT PHASE II	TW	100	40			91	P
404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353105 SHINDE PRAJAKTA RAJENDRA JAYASHRI ,71539921H ,VPKBIET ,B120353105

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	33	54	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	45	59	P	P
404182	COMPUTER NETWORKS	PP	100	40	19	35	54	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	44	66	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	39	55	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	46	58	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	55	69	P	C	404192D	WIRELESS NETWORKS	PP	100	40	16	30	46	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	42	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C	404195	PROJECT PHASE II	TW	100	40			91	P	P
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1011/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353106 SHINDE RAHUL JAGANNATH SUNITA ,71505104M ,VPKBIET ,B120353106

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	35	46	P		404189	MOBILE COMMUNICATION	PP	100	40	11	24	--	F	
404182	COMPUTER NETWORKS	PP	100	40	16	30	46	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	20	--	F	
404183	MICROWAVE ENGINEERING	PP	100	40	14	33	47	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	07	33\$	40	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	36	53	P	C	404192D	WIRELESS NETWORKS	PP	100	40	09	34	43	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	10	47	57	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			85	P	P
404188	PROJECT PHASE I	OR	50	20			34	P	C	404195	PROJECT PHASE II	PR	50	20			37	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120353107 SHINDE SHRADDHA PRAFULL ROHINI ,71413883F ,VPKBIET ,B120353107

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	47	58	P		404189	MOBILE COMMUNICATION	PP	100	40	07	40	47	P	P
404182	COMPUTER NETWORKS	PP	100	40	12	30	42	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	44	66	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	38	50	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	06	35	41	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	14	38	52	P	C	404192D	WIRELESS NETWORKS	PP	100	40	06	14	--	F	
404185C	PLCS AND AUTOMATION	PP	100	40	09	34	43	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			29	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C	404195	PROJECT PHASE II	TW	100	40			85	P	P
404188	PROJECT PHASE I	OR	50	20			32	P	C	404195	PROJECT PHASE II	PR	50	20			36	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353108 SINHA DIPESHKUMAR SUDHIRKUMAR POONAM ,71413891G ,VPKBIET ,B120353108

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	05	40	45	P	404189	MOBILE COMMUNICATION	PP	100	40	08	22	--	F
404182	COMPUTER NETWORKS	PP	100	40	12	18	--	F	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	29	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	03	14	--	F	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	36	46	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	32	45	P C	404192D	WIRELESS NETWORKS	PP	100	40	17	28	45	P
404185C	PLCS AND AUTOMATION	PP	100	40	11	41	52	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P C	404195	PROJECT PHASE II	TW	100	40			90	P
404188	PROJECT PHASE I	OR	50	20			34	P C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120353109 SONAWANE TRUPTI PRADEEP PUSHPA ,71539932C ,VPKBIET ,B120353109

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	33	52	P C	404189	MOBILE COMMUNICATION	PP	100	40	16	33	49	P
404182	COMPUTER NETWORKS	PP	100	40	24	30	54	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	53	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	41	57	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	44	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	49	59	P C	404192D	WIRELESS NETWORKS	PP	100	40	20	40	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	48	71	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P C	404195	PROJECT PHASE II	TW	100	40			93	P
404188	PROJECT PHASE I	OR	50	20			39	P C	404195	PROJECT PHASE II	PR	50	20			44	P

GRAND TOTAL = 1026/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353110 SONTAKKE SAYALI DATTATRAY GEETA ,71638466D ,VPKBIET ,B120353110

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	41	59	P C	404189	MOBILE COMMUNICATION	PP	100	40	16	44	60	P
404182	COMPUTER NETWORKS	PP	100	40	26	32	58	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	48	75	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	49	74	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	52	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	43	55	P C	404192D	WIRELESS NETWORKS	PP	100	40	25	37	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	26	63	89	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P C	404195	PROJECT PHASE II	TW	100	40			91	P
404188	PROJECT PHASE I	OR	50	20			42	P C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = 1085/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353111 SURVASE VIPIN VIKAS REKHA ,71539935H ,VPKBIET ,B120353111

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	31	48	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	28	44	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	31	43	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	06	38	44	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	45	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	33	41	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	36	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	35	43	P	
404192D	WIRELESS NETWORKS	PP	100	40	08	28	36#	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 911/1500 , RESULT:FIRST CLASS [# 0.4]

B120353112 SURYAKAR JYOTIBA BALAJI CHANDRAKALA ,71638467B ,VPKBIET ,B120353112

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	43	54	P	
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P	
404183	MICROWAVE ENGINEERING	PP	100	40	11	33	44	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	38	48	P	
404185C	PLCS AND AUTOMATION	PP	100	40	15	31	46	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	37	45	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	31	48	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	39	51	P	
404192D	WIRELESS NETWORKS	PP	100	40	06	29	35#	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 876/1500 , RESULT:HIGHER SECOND CLASS [# 0.4]

RESULT RESERVED FOR BACKLOGS.

B120353113 SURYAVANSHI SHIVAJI SANTUKRAO MANGALBAI ,71539937D ,VPKBIET ,B120353113

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	32	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	31	45	P	
404183	MICROWAVE ENGINEERING	PP	100	40	26	35	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	55	66	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	38	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	34	49	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	40	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	37	51	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	30	48	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 991/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F: PASS/FAIL, C: PREVIOUS CARRY OVER

B120353114 SURYAWANSHI VISHAL VILASRAO PARVATI ,71539940D ,VPKBIET ,B120353114

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	32	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	38	55	P	
404183	MICROWAVE ENGINEERING	PP	100	40	14	35	49	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	37	50	P	
404185C	PLCS AND AUTOMATION	PP	100	40	17	36	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	44	56	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	46	72	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	36	48	P	
404192D	WIRELESS NETWORKS	PP	100	40	15	28	43	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 929/1500 , RESULT:FIRST CLASS

B120353115 SWAMI KARUNA BASWARAJ ANITA ,71638468L ,VPKBIET ,B120353115

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	44	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	39	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	38	57	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	51	64	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	23	38	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	46	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	49	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	46	60	P	
404192D	WIRELESS NETWORKS	PP	100	40	21	38	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1064/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353116 SWAMI VISHWESHWAR MALLIKARJUN MANKARNA ,71413907G ,VPKBIET ,B120353116

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	45	55	P	
404182	COMPUTER NETWORKS	PP	100	40	16	43	59	P	
404183	MICROWAVE ENGINEERING	PP	100	40	15	28	43	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	06	39	45	P	
404185C	PLCS AND AUTOMATION	PP	100	40	12	38	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	07	33\$	40	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	34	56	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	35	45	P	
404192D	WIRELESS NETWORKS	PP	100	40	09	31\$	40	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 868/1500 , RESULT:HIGHER SECOND CLASS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353117 TAKSALE AMRUTA MILIND MUGDHA ,71539942L ,VPKBIET ,B120353117

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	37	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	38	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	11	29	40	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	36	53	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	47	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	32	46	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	43	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	39	51	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	40	57	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 984/1500 , RESULT:FIRST CLASS

B120353118 TAWARE SHON PRAVIN DIPALI ,71539949H ,VPKBIET ,B120353118

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	34	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	31	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	41	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	59	76	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	19	65	84	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			46	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	33	48	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	44	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	42	57	P	
404192D	WIRELESS NETWORKS	PP	100	40	15	45	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1097/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353119 THOMBARE ANKITA ASHOK SAVITA ,71539952H ,VPKBIET ,B120353119

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	33	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	45	69	P	
404183	MICROWAVE ENGINEERING	PP	100	40	16	38	54	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	41	54	P	
404185C	PLCS AND AUTOMATION	PP	100	40	17	33	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	31\$	40	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	22	--	F	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	28	42	P	
404192D	WIRELESS NETWORKS	PP	100	40	17	36	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			39	P	

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353123 VISHAL VILAS NIKAM VIJAYA ,71539966H ,VPKBIET ,B120353123																	
SEM.:1					SEM.:2												
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	30	49	P C	404189	MOBILE COMMUNICATION	PP	100	40	14	35	49	P
404182	COMPUTER NETWORKS	PP	100	40	22	37	59	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	42	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	36	50	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	34	47	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	47	60	P C	404192D	WIRELESS NETWORKS	PP	100	40	21	38	59	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	54	76	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P C	404195	PROJECT PHASE II	TW	100	40			97	P
404188	PROJECT PHASE I	OR	50	20			42	P C	404195	PROJECT PHASE II	PR	50	20			46	P

GRAND TOTAL = 1012/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353124 WAGHMODE POOJA DATTATRAY ANITA ,71539969B ,VPKBIET ,B120353124																	
SEM.:1					SEM.:2												
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	46	59	P C	404189	MOBILE COMMUNICATION	PP	100	40	13	30	43	P
404182	COMPUTER NETWORKS	PP	100	40	11	30	41	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	32	51	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	35	47	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	30&	40	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	48	63	P	404192D	WIRELESS NETWORKS	PP	100	40	05	35&	40	P
404185C	PLCS AND AUTOMATION	PP	100	40	14	28	42	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P C	404195	PROJECT PHASE II	TW	100	40			96	P
404188	PROJECT PHASE I	OR	50	20			40	P C	404195	PROJECT PHASE II	PR	50	20			44	P

GRAND TOTAL = 899 + 01/1500 , RESULT:FIRST CLASS [& O.163]

RESULT RESERVED FOR BACKLOGS.

B120353125 YADAV KISHOR SAMBHAJI VANMALA ,71314010M ,VPKBIET ,B120353125																	
SEM.:1					SEM.:2												
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	33	45	P C	404189	MOBILE COMMUNICATION	PP	100	40	11	30	41	P
404182	COMPUTER NETWORKS	PP	100	40	19	36	55	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	07	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	17	35	52	P	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	21	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	42	52	P C	404192D	WIRELESS NETWORKS	PP	100	40	10	30\$	40	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	36	49	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P C	404195	PROJECT PHASE II	TW	100	40			80	P
404188	PROJECT PHASE I	OR	50	20			30	P C	404195	PROJECT PHASE II	PR	50	20			30	P

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ O.1]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353126 ZARGAD SHUBHANGI SURESH NIRMALA ,71539975G ,VPKBIET ,B120353126

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	55	72	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	47	61	P	P
404182	COMPUTER NETWORKS	PP	100	40	20	35	55	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	31	57	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	45	65	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	35	48	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	47	61	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	32	51	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	39	61	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			90	P	P
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1020/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120353127 ZINGADE PRIYA ANKUSH SMITA ,71539977C ,VPKBIET ,B120353127

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	30	43	P	C	404189	MOBILE COMMUNICATION	PP	100	40	13	30	43	P	P
404182	COMPUTER NETWORKS	PP	100	40	21	33	54	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	30	55	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	37	53	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	32	45	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	06	43	49	P	C	404192D	WIRELESS NETWORKS	PP	100	40	13	32	45	P	P
404185C	PLCS AND AUTOMATION	PP	100	40	09	45	54	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 925 + 10/1500 , RESULT:FIRST CLASS [& O.163]

B120353128 JADHAV SUSHMITA RAVINDRA SANGITA ,71313712G ,VPKBIET ,B120353128

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	AA	--	F		404189	MOBILE COMMUNICATION	PP	100	40	01	39\$	40	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	44	65	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	55	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	06	38	44	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	54	64	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	50	69	P	C	404192D	WIRELESS NETWORKS	PP	100	40	22	46	68	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	44	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			31	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			23	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			25	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			25	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			25	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			26	P	C	404195	PROJECT PHASE II	TW	100	40			87	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C	404195	PROJECT PHASE II	PR	50	20			37	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353131 PATIL DASRAO SUBHASHRAO VANDANA ,71212642C ,VPKBIET ,B120353131

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30\$	40	P
404182	COMPUTER NETWORKS	PP	100	40	16	28	44	P C
404183	MICROWAVE ENGINEERING	PP	100	40	11	33	44	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	09	37	46	P C
404185C	PLCS AND AUTOMATION	PP	100	40	10	32	42	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			26	P C
404188	PROJECT PHASE I	OR	50	20			38	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	AA	--	F
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	11	17	--	F
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	AA	--	F
404192D	WIRELESS NETWORKS	PP	100	40	14	41	55	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P C
404195	PROJECT PHASE II	TW	100	40			85	P C
404195	PROJECT PHASE II	PR	50	20			40	P C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

B120353132 PATIL YUVRAJ JAYAWANT SUREKHA ,71413813E ,VPKBIET ,B120353132

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	38	47	P C
404182	COMPUTER NETWORKS	PP	100	40	15	36	51	P C
404183	MICROWAVE ENGINEERING	PP	100	40	07	35	42	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	07	37	44	P C
404185C	PLCS AND AUTOMATION	PP	100	40	17	38	55	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P C
404188	PROJECT PHASE I	OR	50	20			36	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	44	52	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	31	47	P C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	28	40	P C
404192D	WIRELESS NETWORKS	PP	100	40	17	41	58	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P C
404195	PROJECT PHASE II	TW	100	40			87	P C
404195	PROJECT PHASE II	PR	50	20			37	P C

GRAND TOTAL = 886/1500 , RESULT:HIGHER SECOND CLASS