

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353002 CHAVLE ANJALI BALIRAM KAMALABAI ,71539623E ,VPKBIET ,B120353002

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	31	46	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	31	45	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	28	40	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	37	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	28	40	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	09	31\$	40	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	30\$	40	P		404192D	WIRELESS NETWORKS	PP	100	40	09	31\$	40	P	
404185C	PLCS AND AUTOMATION	PP	100	40	11	10	--	F		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			91	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120353004 GURME ROHINI BABURAO ANITA ,71539688K ,VPKBIET ,B120353004

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	39	50	P	C	404189	MOBILE COMMUNICATION	PP	100	40	03	07	--	F	
404182	COMPUTER NETWORKS	PP	100	40	11	29	40	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	28	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	03	10	--	F		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	35	43	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	32	40	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	33	52	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	29	46	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			32	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			28	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			43	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120353007 KAPALE SUCHIT SURESHRAO REKHA ,71454045F ,VPKBIET ,B120353007

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30	40	P	C	404189	MOBILE COMMUNICATION	PP	100	40	05	AA	--	F	
404182	COMPUTER NETWORKS	PP	100	40	14	38	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	51	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	29	42	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	11	58	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	07	AA	--	F		404192D	WIRELESS NETWORKS	PP	100	40	20	36	56	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	35	49	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			21	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			22	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			21	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			80	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS

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B120353008 NAVTAKE SATYAM RAMAKANT DHANSHREE ,71638459M ,VPKBIET ,B120353008

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	31	44	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	33	47	P	
404182	COMPUTER NETWORKS	PP	100	40	20	29	49	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	28	42	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	11	42	53	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	34	46	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	44	64	P	C	404192D	WIRELESS NETWORKS	PP	100	40	12	31	43	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	22	32	54	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			80	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = 904/1500 , RESULT:FIRST CLASS

B120353009 PATIL DASRAO SUBHASHRAO VANDANA ,71212642C ,VPKBIET ,B120353009

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30\$	40	P	C	404189	MOBILE COMMUNICATION	PP	100	40	10	30\$	40	P	
404182	COMPUTER NETWORKS	PP	100	40	16	28	44	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	11	29	40	P	
404183	MICROWAVE ENGINEERING	PP	100	40	11	33	44	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	41	49	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	09	37	46	P	C	404192D	WIRELESS NETWORKS	PP	100	40	14	41	55	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	10	32	42	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			26	P	C	404195	PROJECT PHASE II	TW	100	40			85	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = 847/1500 , RESULT:HIGHER SECOND CLASS [\$ 0.1]

B120353010 SHINDE RAHUL JAGANNATH SUNITA ,71505104M ,VPKBIET ,B120353010

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	35	46	P	C	404189	MOBILE COMMUNICATION	PP	100	40	11	29	40	P	
404182	COMPUTER NETWORKS	PP	100	40	16	30	46	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	20	--	F	
404183	MICROWAVE ENGINEERING	PP	100	40	14	33	47	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	07	33\$	40	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	36	53	P	C	404192D	WIRELESS NETWORKS	PP	100	40	09	34	43	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	10	47	57	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			85	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C	404195	PROJECT PHASE II	PR	50	20			37	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

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B120353012 SINHA DIPESHKUMAR SUDHIRKUMAR POONAM ,71413891G ,VPKBIET ,B120353012

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	05	40	45	P	C	404189	MOBILE COMMUNICATION	PP	100	40	13	03	--	F	
404182	COMPUTER NETWORKS	PP	100	40	12	00	--	F		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	29	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	04	AA	--	F		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	36	46	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	32	45	P	C	404192D	WIRELESS NETWORKS	PP	100	40	17	28	45	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	11	41	52	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C	404195	PROJECT PHASE II	TW	100	40			90	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120353014 THOMBARE ONKAR PAVAN ROHINI ,71313984G ,VPKBIET ,B120353014

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	40	50	P	C	404189	MOBILE COMMUNICATION	PP	100	40	10	30\$	40	P	
404182	COMPUTER NETWORKS	PP	100	40	19	28	47	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	28\$	45	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	28	41	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	07	34	41	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	30&	40	P	C	404192D	WIRELESS NETWORKS	PP	100	40	10	31	41	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	33	47	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			87	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = 819 + 02/1500 , RESULT:SECOND CLASS [& 0.163][\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120353015 VHATKAR SACHIN SUKHDEO SHAMAL ,71638469J ,VPKBIET ,B120353015

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	28	44	P	C	404189	MOBILE COMMUNICATION	PP	100	40	09	31\$	40	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	28	49	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	29	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	32	49	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	09	35	44	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	37	45	P	C	404192D	WIRELESS NETWORKS	PP	100	40	08	47	55	P	
404185C	PLCS AND AUTOMATION	PP	100	40	07	45	52	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			26	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			85	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C	404195	PROJECT PHASE II	PR	50	20			35	P	C

GRAND TOTAL = 853/1500 , RESULT:HIGHER SECOND CLASS [\$ 0.1]