

SAVITRIBAI PHULE PUNE UNIVERSITY ,B.E.(2012 COURSE) EXAMINATION, APR/MAY 2020
 CENTRE: [CEGP011510] - V.P KAMALNAYAN BAJAJ INSTITUTE OF ENGINEERING & TECHNOLOGY, BARAMATI
 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMMUNICATION)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120353001 CHAVLE ANJALI BALIRAM KAMALABAI ,71539623E ,VPKBIET[35] ,B120353001

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	31	46	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	31	45	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	28	40	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	37	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	28	40	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	09	31\$	40	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	30\$	40	P	C	404192D	WIRELESS NETWORKS	PP	100	40	09	31\$	40	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	11	70	81	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			91	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	C

GRAND TOTAL = 900/1500 , RESULT:FIRST CLASS [\$ 0.1]

ORD1MARKS : (404184A=>2,404191C=>2,404192D=>3)

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B120353002 GURME ROHINI BABURAO ANITA ,71539688K ,VPKBIET[35] ,B120353002

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	39	50	P	C	404189	MOBILE COMMUNICATION	PP	100	40	03	39	42	P	
404182	COMPUTER NETWORKS	PP	100	40	11	29	40	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	28	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	03	37	40#	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	35	43	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	32	40	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	33	52	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	17	29	46	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			32	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			28	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			43	P	C

GRAND TOTAL = 830/1500 , RESULT:HIGHER SECOND CLASS [# 0.4]

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B120353003 KAPALE SUCHIT SURESHRAO REKHA ,71454045F ,VPKBIET[35] ,B120353003

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30	40	P	C	404189	MOBILE COMMUNICATION	PP	100	40	08	45	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	38	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	51	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	29	42	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	11	58	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	07	64	71	P		404192D	WIRELESS NETWORKS	PP	100	40	20	36	56	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	35	49	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			21	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			22	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			21	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			80	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = 898 + 02/1500 , RESULT:FIRST CLASS [0.2]

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 B120353004 SINHA DIPESHKUMAR SUDHIRKUMAR POONAM ,71413891G ,VPKBIET[35] ,B120353004

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	05	40	45	P	C	404189	MOBILE COMMUNICATION	PP	100	40	13	55	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	70	82	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	29	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	05	64	69	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	10	36	46	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	32	45	P	C	404192D	WIRELESS NETWORKS	PP	100	40	17	28	45	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	11	41	52	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C	404195	PROJECT PHASE II	TW	100	40			90	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = 915/1500 , RESULT:FIRST CLASS

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